



VEBKO ONLINE HELP

Introduceing Internal Modules Part2

Category	tester
Section	Troubleshooting
Language	English
Generated	2026-10-18

When the device is electrified in this state, after the electricity passes the lamp series, it enters the incomplete Main Switching through this phoenix and crosses these protective elements. In the end, this Main has two 220-volt outputs and one of the outputs enters the complete switching side from this way and the other output enters the incomplete switching side through this phoenix. When the electricity reaches this part, it enters the PS through this black pin header. This PS receives the 220 volts AC and converts it to 5 volts and 12 volts DC and provides the upper modules with power supply. These 5 volts enters the micro and is converted to 3/3 volts by this IC; using this 3/3 volts electricity micro LPC1788 which is the main micro of the device starts working.

In this part where the phoenixes are electrified, the middle part of the device is not electrified yet and only the digital part of the device is electrified. Then it is ready to command the middle part of the switching by the micro. Above the phoenix there is a glass relay which belongs to the complete switching side and is known as switch number two in the software. On this side, this glass relay is the relay which activates by connecting switch number one in the software and electrifies this part of the switching. Here there is a series lamp in the circuit. When the switches activate, the lamp turns on for a moment and then it turns off. It is better that there is a series lamp series in the circuit because if current is drawn from a part of the circuit, this lamp is turned on.

As soon as the device is electrified the digital part enters the micro by 5-volt and 12-volt PS and prepares the micro module for issuing the command. In this part, we enter the settings of the software and connect to the device by entering the IP. , In Hardware tab there are two sections of Enable Switch 1 and Enable Switch 2 and these are the relays that were explained.

[\[jg1\]Introduce_Internal_Modules_Part2](#)