



VEBKO ONLINE HELP

Continuous Ramp Type

Category	tester
Section	AMT Sequencer Module
Language	English
Generated	2026-10-18

This "State Type" is similar to "Step Ramp". The only difference is that in "Step Ramp" the parameter changes step by step at user-defined time intervals but in "Continuous Ramp", the parameters increase or decrease continuously with 400 microseconds time steps.



Figure 1

The parameters adjusted for "Continuous Ramp" include start value, final value and total time and the start values are specified in "Start Values" table. Here, the values for voltage signals are 5, 10 and 15 volts. "Binary Output", "Binary Input" and "Other Setting" sections have been described before and we can skip them. In "Final Values" table the final value of the parameters is specified. Here, the values for voltage signals are 20, 25 and 30 volts.

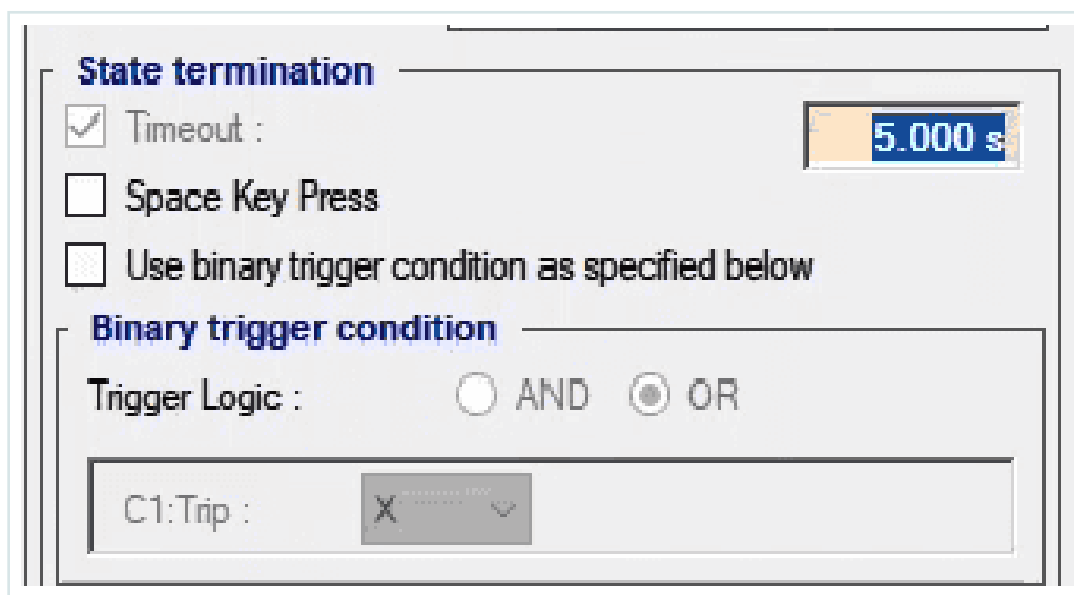
Start Value			
Signal	Amplitude	Phase	Freque...
V L1-E: V L1-E	5.000 V	0.00 °	50.00 Hz
V L2-E: V L2-E	10.00 V	-120.00 °	50.00 Hz
V L3-E: V L3-E	15.00 V	120.00 °	50.00 Hz
I L1: I L1	1.000 A	0.00 °	50.00 Hz
I L2: I L2	1.000 A	-120.00 °	50.00 Hz
I L3: I L3	1.000 A	120.00 °	50.00 Hz

Figure 2

Final Values			
Signal	Amplitude	Phase	Freque...
V L1-E: V L1-E	20.00 V	0.00 °	50.00 Hz
V L2-E: V L2-E	25.00 V	-120.00 °	50.00 Hz
V L3-E: V L3-E	30.00 V	120.00 °	50.00 Hz
I L1: I L1	1.000 A	0.00 °	50.00 Hz
I L2: I L2	1.000 A	-120.00 °	50.00 Hz
I L3: I L3	1.000 A	120.00 °	50.00 Hz

Figure 3

In "Total Time" field in the "Continuous Ramp Setting" section the total time of the "State" is specified which is linked with the "Timeout" time in "State Termination" in "Trigger" setting. "Trigger", "Offset Value", "Error/Trigger Overcurrent" and "Disable Relay" sections have been described in previous videos and more descriptions should not be necessary here.



State termination

☒ Timeout : 5.000 s

☐ Space Key Press

☐ Use binary trigger condition as specified below

Binary trigger condition

Trigger Logic : ☐ AND ☒ OR

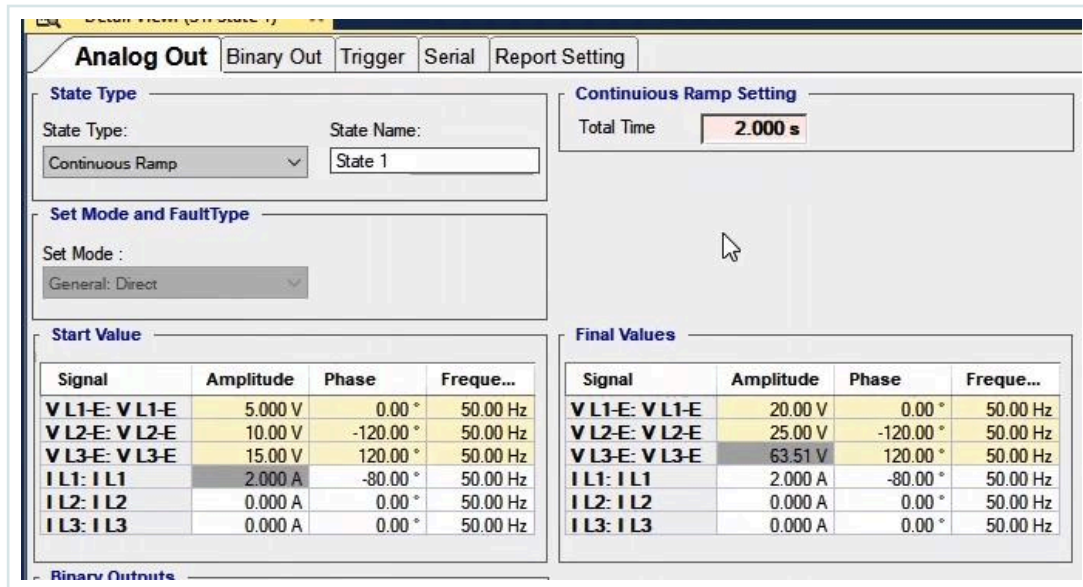
C1: Trip : ☒

Figure 4

Another important matter about "Ramp" is that, in addition to put ramp on voltage and current, in "Step Ramp" the user can put ramp on impedance by changing the "Set Mode". Since the hardware is only capable of recognizing voltage and current and cannot recognize the impedance parameter, and because there are so many impedance parameters, it is not possible to send these parameters to the hardware in every "Step". So, in "Step Ramp" firstly all values of impedance steps are sent to the software and then measured voltage and current sent to the hardware as an array every time "Step" in "Continuous Ramp" is 400 microseconds, the amount of calculations and arrays is greatly increased and it is not possible for the results of the calculations and created arrays to be sent to the hardware. So, this "State Type" is always carried out in "General: Direct" mode.

Signal	Amplitude	Phase	Freque...
V L1-E: V L1-E	2.000 V	0.00 °	50.00 Hz
V L2-E: V L2-E	63.51 V	-120.00 °	50.00 Hz
V L3-E: V L3-E	63.51 V	120.00 °	50.00 Hz
I L1: I L1	2.000 A	-80.00 °	50.00 Hz
I L2: I L2	0.000 A	0.00 °	50.00 Hz
I L3: I L3	0.000 A	0.00 °	50.00 Hz

Figure 5



Analog Out Binary Out Trigger Serial Report Setting

State Type

State Type: Continuous Ramp State Name: State 1

Continuous Ramp Setting

Total Time: 2.000 s

Set Mode and FaultType

Set Mode: General: Direct

Start Value

Signal	Amplitude	Phase	Frequ...
V L1-E: V L1-E	5.000 V	0.00 °	50.00 Hz
V L2-E: V L2-E	10.00 V	-120.00 °	50.00 Hz
V L3-E: V L3-E	15.00 V	120.00 °	50.00 Hz
I L1: I L1	2.000 A	-80.00 °	50.00 Hz
I L2: I L2	0.000 A	0.00 °	50.00 Hz
I L3: I L3	0.000 A	0.00 °	50.00 Hz

Final Values

Signal	Amplitude	Phase	Frequ...
V L1-E: V L1-E	20.00 V	0.00 °	50.00 Hz
V L2-E: V L2-E	25.00 V	-120.00 °	50.00 Hz
V L3-E: V L3-E	63.51 V	120.00 °	50.00 Hz
I L1: I L1	2.000 A	-80.00 °	50.00 Hz
I L2: I L2	0.000 A	0.00 °	50.00 Hz
I L3: I L3	0.000 A	0.00 °	50.00 Hz

Binary Outputs

Figure 6

[jg1]Continuous_Ramp_Type